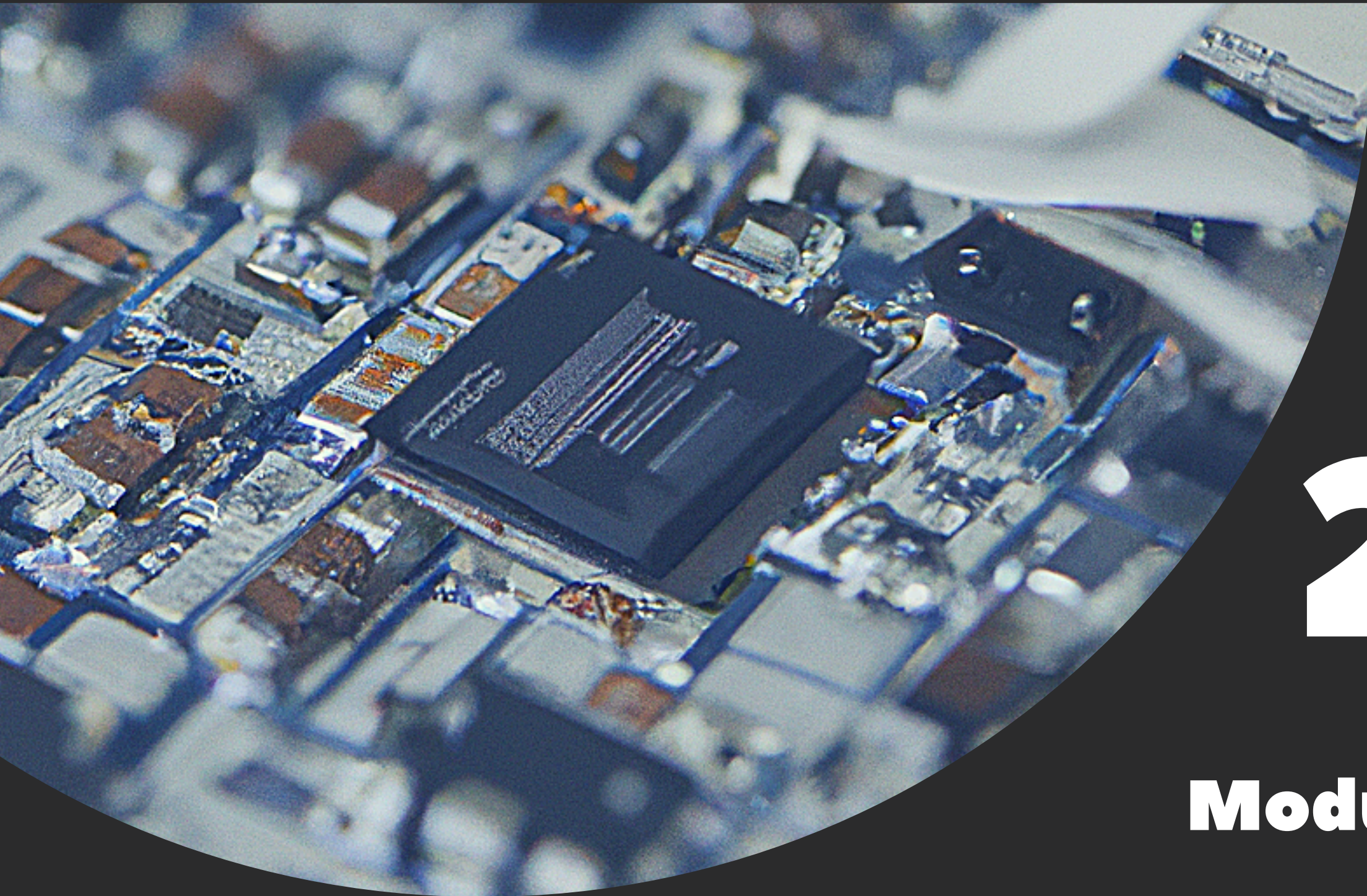


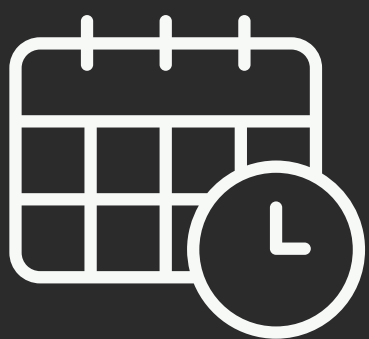
Taiwan Semiconductor Summer Program



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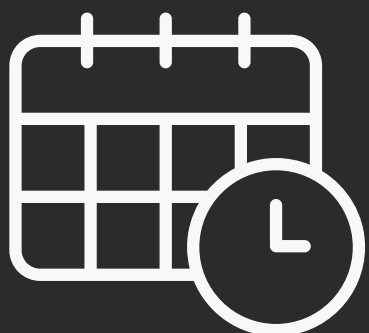
Module

Forum



Session I
Aug 6 ~ Aug 19

IC Design Program: National Taiwan University
Materials Program: National Yang Ming Chiao Tung University



Session II
Aug 20 ~ Aug 26

Semiconductor Forum: Taiwan Semiconductor Research Institute (TSRI)

Flight tickets (two ways) and daily allowance will be covered by the Ministry of Foreign Affairs, Republic of China (Taiwan).



中華民國外交部
MINISTRY OF FOREIGN AFFAIRS
REPUBLIC OF CHINA (TAIWAN)



National
Taiwan
University



國立陽明交通大學
NYCU



NAR Labs 國家實驗研究院
台灣半導體研究中心
Taiwan Semiconductor Research Institute



IC Designs Learning Program

Taipei

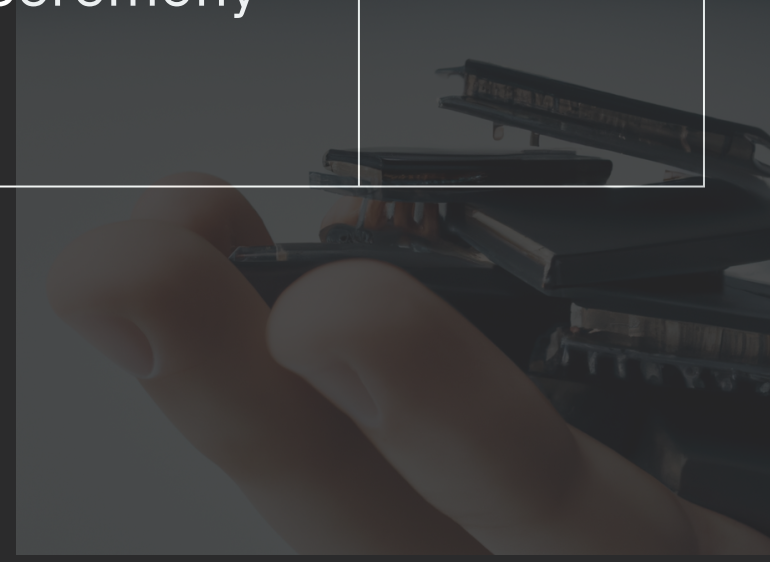


AUG 6 ~ AUG 19

SCHEDULE ARRANGEMENT

Week 1						
Sun	Mon	Tue	Wed	Thu	Fri	Sat
Arrival	Orientation/ Campus Tour/ Welcome Lunch	Introduction of Semiconductor in Taiwan & Program Overview	Basic Semiconductor Physics	Semiconductor Transistors for Logic and Memory	NTU Lab & Library Visit	Discovery Taiwan
	NTU Introduction & Innovation Team- Building Workshop	Introduction to Semiconductor Manufacturing Technology			Exploring Taiwan: A Journey Through the Past to Modern Society	
Week 2						
Sun	Mon	Tue	Wed	Thu	Fri	Sat
Discovery Taiwan	Digital Integrated Circuit Designs	Analog Integrated Circuit Designs	Electronic Design Automation	Application of IC Design	The Future of Semiconductor Electronics	Check-out & Departure to TSRI
				Historical & Culture Tour - National Palace Museum Night Market Adventure	Final Presentation & Closing Ceremony	

*The schedule is subject to change.





Materials Learning Program



AUG 6 ~ AUG 19

SCHEDULE ARRANGEMENT

Week 1						
Sun	Mon	Tue	Wed	Thu	Fri	Sat
Arrival	Orientation/ Campus Tour/ Welcome Lunch	Introduction of Semiconductor	Oxidation Process	Lithography Process	Visit: TSMC Museum of Innovation	Discovery Taiwan
	Introduction of NYCU & Library Tour	Safety Issues & Clean Process	Deposition Process	Lab Visit	Beipu Culture Trip: Make Your Own Lei Cha	
Week 2						
Sun	Mon	Tue	Wed	Thu	Fri	Sat
Discovery Taiwan	Group Discussion	Fabrication of PN Diodes	Fabrication of MOS Capacitors	Electrical Measurement of PN Diodes	Electrical Measurement of MOS capacitors and MOSFETs	Preparing for TSRI Forum
	Etching Process & Doping Process	Lab: Observation and Operation	Fabrication of MOSFETs	Exploring Taiwan: Hsinchu City God Temple (Chenghuang Temple) & Glass Museum	Final Presentation & Closing Ceremony	

*The schedule is subject to change.





TSRI Forum



AUG 20 ~ AUG 26

SCHEDULE ARRANGEMENT

Week 1				
Mon	Tue	Wed	Thu	Fri
Opening and Kick-off	<Subject II> Cloud and Design Service in TSRI	<Subject III> Material & Topology Analysis (A)	<Keynote> The Ecosystem of Semiconductor Technology in Taiwan	<Short Course> IC Design Including 5 topics, from fundamental to advanced levels.
<Subject I> Process and Integration Capabilities in TSRI				
<Subject I> Integration Platforms in TSRI	<Subject II> AI and SoC Design Platform	<Subject III> Material & Topology Analysis (B)	<Keynote> Talent Cultivation for Global Semiconductor Talents	
<Subject I> Lab Tour	<Subject II> Silicon Photonics Design Platform	<Subject IV> High-frequency Device Measurement Platform	<Site Visit>	
	<Subject II> MEMS and Sensor Integration	<Subject IV> Semiconductor Device Measurement Platform	<Site Visit>	

*The schedule is subject to change.

